

Spartan 3

ELBERT V2 is a simple but versatile FPGA Learning/Development board featuring Xilinx Spartan 3A FPGA. An excellent choice for beginners and advanced learners for experimenting and learning system design with FPGAs. This development board features Xilinx XC3S50A 144 pin FPGA with a maximum of 108 user IOs (Some IOs are dedicated to system and peripherals). USB interface provides a fast and easy configuration download to the onboard SPI flash. You don't need a programmer or special downloader cable to download bitstream to Elbert. ELBERT V2 features a stable clock source that is derived from the onboard configuration controller. ELBERT V2 incorporates LEDs, switches, and other peripherals for curious users to get started with the "Hello World" program in a matter of minutes.

Features

- FPGA: Spartan XC3S50A in TQG144 package
- Flash memory: 16 Mb SPI flash memory (M25P16)
- USB 2.0 interface for On-board flash programming
- FPGA configuration via JTAG and USB
- 8 LEDs, Six Push Buttons and 8-way DIP switch for user-defined application
- VGA output
- Stereo audio out
- Micro SD Card Adapter
- Three Seven Segment Displays
- 39 IOs for user-defined purposes
- On-board voltage regulators for single power rail operation
- RoHS Compliant

Applications

- Product Prototype Development
- Home Networking
- Signal Processing
- Wired and Wireless Communications
- Educational tool for schools and universities